

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Ehben et al.
Filed : Concurrently herewith
Title : Integrated Circuit Having Capacitive Elements



#5
1-3-01
Hallman

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent No. 5,396,198 (Yamashita et al.), dated March 7, 1995;

U.S. Patent No. 5,606,197 (Johansson et al.), dated February 25, 1997;

German Published, Non-Prosecuted Patent Application No. DE 43 22 354 A1 (Johansson et al.), dated January 13, 1994, condenser in an integrated functional block or in an integrated circuit of large capacity; method for producing a condenser and use of the condenser as an integrated decoupling condenser;

Published, European Patent Application No. 0 163 384 A1 (Horiguchi), dated December 4, 1985;

Published, European Patent Application No. EP 0 908 950 A2 (Ehben), dated April 14, 1999, integrated circuit;

Published, European Patent Application No. 0 656 657 A1 (Beyron), dated November 30, 1994, device for reducing the noise level of an integrated circuit to several levels of conductors;

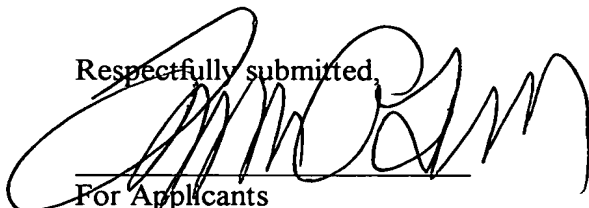
Japanese Patent Application No. 04-127464 A (Yano), dated April 28, 1992, and
corresponding abstract;

Japanese Patent Application No. 02-250370 A (Takekoshi), dated October 8, 1990, and
corresponding abstract;

Japanese Patent Application No. 09-246276 (Masahiro), dated September 19, 1997, and
corresponding abstract;

If no translation of pertinent portions of any foreign language patents or publications
mentioned above is included with the aforementioned copies of those applications, patents
and/or publications, it is because no existing translation is readily available to the applicant.

Respectfully submitted,



For Applicants

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